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HABIB UNIVERSITY'S SCHOOL OF SCIENCE AND ENGINEERING PRESENTS

# VIRTUAL PROTOTYPING FOR HARDWARE AND SOFTWARE DESIGN

**SPEAKER: SAAD SIDDIQUI**

Saad has done B.E (Electronics) from NED University of Engineering and Technology. He went to Germany for higher studies and got his M.Sc. in Communication Engineering from Technical University of Munich (TUM), Germany. His M.Sc. thesis was Design Space Exploration for Multiprocessor Architecture using Virtual Prototypes. For more than 3 years he is working in Intel Germany as Virtual Prototyping Engineer.



## **ABSTRACT:**

This talk will be comprises of modern ways of doing Hardware and Software design by using Virtual Prototypes which not only helps reduce Time-To-Market (TTM) of products but also helps in catching bugs in early stage of design. In first half of the talk, comparison between traditional methodology vs Virtual Prototypes will be discussed and advantages of Virtual Prototypes for both Hardware and Software design. Methodology involved in Virtual Prototypes which consists of SystemC and TLM Modeling will be discussed in second half and also difference between Virtual Prototypes for Hardware Design and Software Design.

Tuesday, February 9<sup>th</sup> 2016, 12:45pm to 1:45pm, Soorty Lecture Hall, Habib University